

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	190	optimi\$7 same ((processor microprocessor) adj design)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/22 14:44
L2	109	1 and @ad<="20001101"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/22 14:56
L3	1	(distributed adj simulation adj parts)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/22 15:46
L4	826	703/14.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/22 15:46
S1	129	("703".clas. "716".clas. "345".clas. "717".clas.) and (pipelin\$3 adj3 (graphical visuali\$7 represent\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 11:10
S2	46	S1 and (simulat\$4 emulat\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 12:18
S3	5	pipelin\$4 adj3 visualization	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 12:19
S4	57	pipelin\$4 adj3 debug\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 12:29
S5	1	(processor adj simulat\$4) and (pipeline adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 12:31

S6	2	(processor same simulat\$4) and (pipeline adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 12:31
S7	2	(cpu same simulat\$4) and (pipeline adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 12:31
S8	4	(circuit same simulat\$4) and (pipeline adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 12:36
S9	6671	("712".clas. "703".clas. "716".clas. ) and pipelin\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 12:36
S10	59	S9 and (pipelin\$4 adj3 (display view visualization screen window))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/10 12:37
S11	1	"09/993074"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/22 14:44